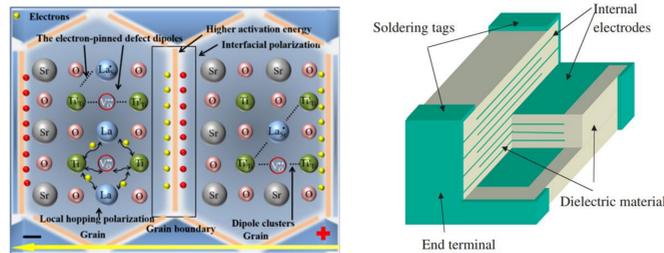


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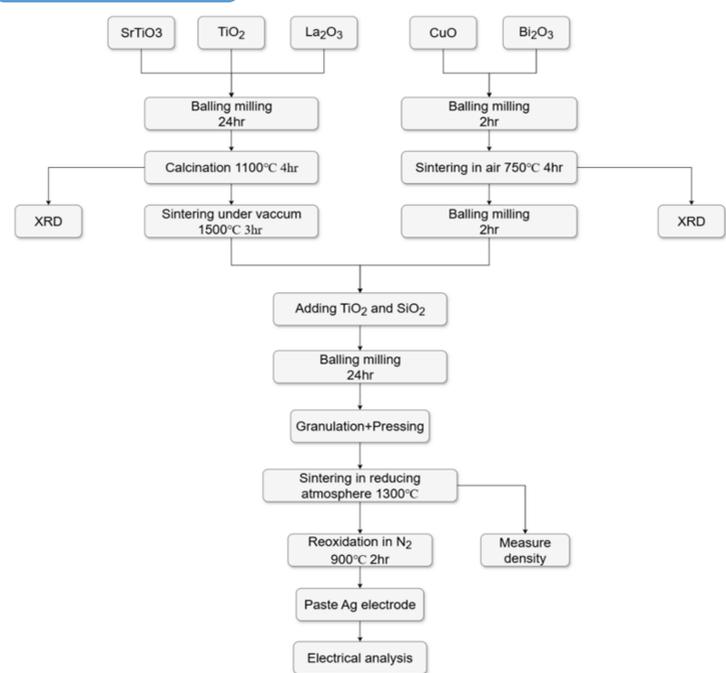
## Introduction

Multilayer ceramic capacitors (MLCCs) are composed of alternately stacked dielectric layers and internal electrodes, which are categorized into precious metals (Pd, Pt) and base metals (Ni, Cu). Considering cost, Ni is commonly employed as the internal electrode; however, its relatively low melting point (~1455 °C) is incompatible with the high-temperature, low-oxygen-partial-pressure conditions required for sintering colossal dielectric materials, resulting in difficulties in co-sintering. To address this issue, this study adopts liquid phase sintering (LPS), in which appropriate additives are introduced to reduce the sintering temperature, thereby preventing Ni melting and dielectric layer degradation. In addition, acceptor doping is employed to regulate grain boundary charge balance and oxygen ion diffusion, optimizing the distribution of oxygen vacancies, suppressing dielectric relaxation, and enhancing the Schottky barrier as well as the insulation resistance.

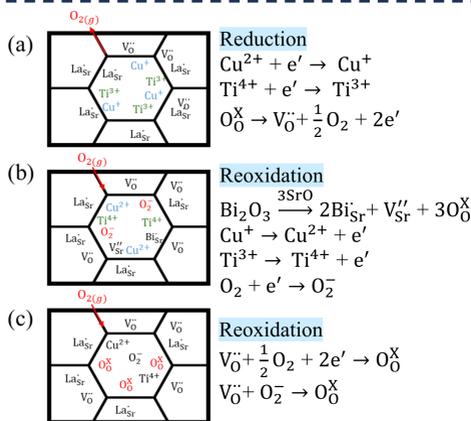
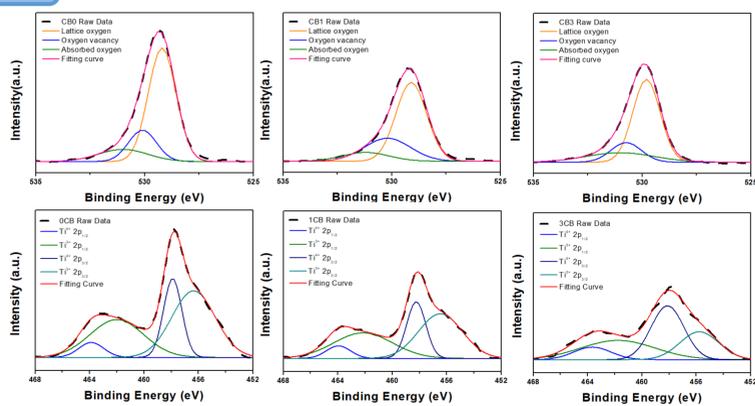
According to previous studies, electron-pinned defect dipoles (EPDD) can be utilized to achieve both colossal permittivity and low dielectric loss. In this work, La was doped into SrTiO<sub>3</sub> to generate defect dipoles Ti'<sub>Ti</sub>-V<sub>O</sub>''-Ti'<sub>Ti</sub> and V<sub>O</sub>''-Ti'<sub>Ti</sub>-La<sub>Sr</sub>' which localize free electrons and suppress their long-range migration. As a result, simultaneous enhancement of the dielectric permittivity and reduction of the dielectric loss were realized.



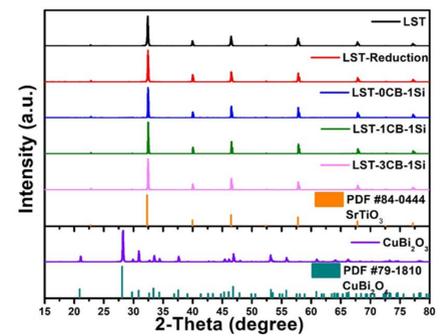
## Experiment



## XPS



## XRD



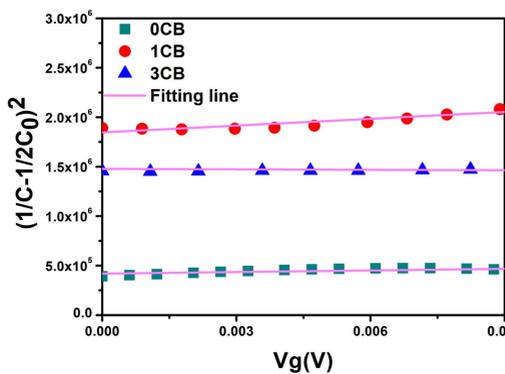
- All samples were all pure SrTiO<sub>3</sub> phase structure with no secondary phases.
- For LST and after reduction LST, lattice volume decreases indicates La<sup>3+</sup> replace of Sr<sup>2+</sup> and due to the generation of abundant oxygen vacancies in reduction.

Sample	Lattice oxygen		Oxygen vacancy		Absorbed oxygen	
	BE (eV)	Area (%)	BE (eV)	Area (%)	BE (eV)	Area (%)
0CB	529.2	70.6	530	23.6	531.6	5.8
1CB	529.1	71.3	530.2	20.8	531.2	7.9
3CB	529.8	74	530.7	17.5	531	8.5

Sample	2p 1/2				2p 3/2				Area ratio of Ti <sup>3+</sup>
	Ti <sup>4+</sup>		Ti <sup>3+</sup>		Ti <sup>4+</sup>		Ti <sup>3+</sup>		
	BE (eV)	Area (%)							
0CB	463.8	7.5	462	19.1	457.9	39.8	456.4	33.6	52.7
1CB	463.9	9	462.1	18.3	458.2	41.3	456.4	31.4	49.7
3CB	463.7	11	461.8	17	458.1	47.4	455.8	24.6	41.6

## Electrical Characterization

### • Double Schottky Barrier

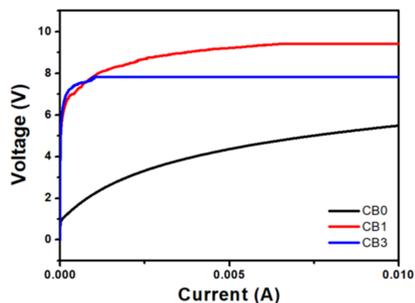


Sample	$\phi_b$ (eV)	$N_d$ (m <sup>-3</sup> )	$N_t$ (m <sup>-2</sup> )	W (nm)
0CB	0.03	1.96E+22	6.21E+14	31.7
1CB	0.08	2.35E+22	7.48E+14	31.8
3CB	0.30	1E+23	3.11E+15	30.9

Mott-Schottky analysis confirms that CB doping markedly increases the Schottky barrier height ( $\phi_b$ ), which strengthens grain-boundary insulation and suppresses space-charge polarization. As a result, dielectric loss ( $\tan \delta$ ) is reduced, the dielectric constant remains stable, and the enhanced barrier structure critically improves varistor behavior with superior nonlinear electrical characteristics.

Donor:  $\text{La}_{\text{Sr}}'$ ,  $\text{V}_O''$ ,  $\text{Bi}_{\text{Sr}}'$   
 Acceptor:  $\text{O}_2^-$ ,  $\text{O}_2^{2-}$ ,  $\text{V}_{\text{Sr}}''$ ,  $\text{Ti}_{\text{Ti}}'$

### • Varistor properties

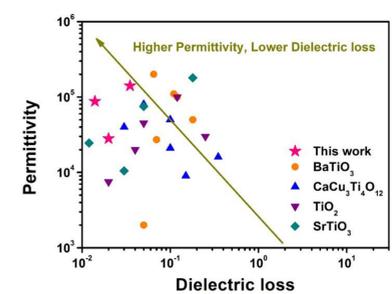
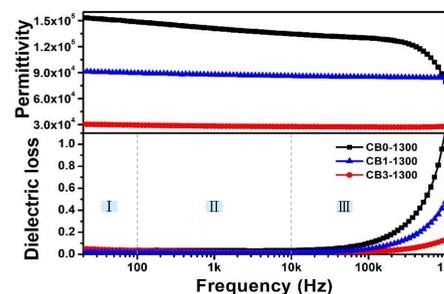


Cu<sup>2+</sup> concentration at grain boundaries enhances the p-type doping effect. This strengthens the potential barrier height, thereby improving both the nonlinear coefficient ( $\alpha$ ) and breakdown voltage ( $V_b$ ), and reduced leakage current ( $I_L$ ).

LST-CB <sub>x</sub> -1Si	$\alpha$	$V_b$ (V/mm)	$I_L$ ( $\mu\text{A}/\text{cm}^2$ )
CB0	3.17	2.95	6.04E-04
CB1	9.64	10.1	1.13E-04
CB3	13.2	10.3	6.73E-05

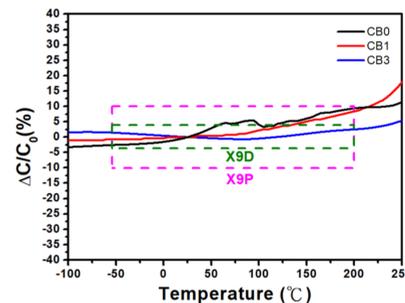
### • Dielectric properties

At low frequency, CuBi<sub>2</sub>O<sub>4</sub> promotes oxygen diffusion and suppresses space charge polarization at the ceramic-electrode interface, reducing dielectric loss. At medium and high frequency, the increased Schottky barrier and EPDD effect restrict electron migration, maintaining a high dielectric constant and suppressing dielectric loss.



Sample	LST-xCB-Si			
CB amount (wt. %)	0	1	3	
Relative Density (%)	89.73	91.39	92.55	
E499	K	141000	876000	28100
0A	Tan $\delta$	0.035	0.014	0.02

### • Temperature Coefficient of Capacitance



All samples demonstrate excellent temperature stability, satisfying the X9P capacitor temperature coefficient specification.

The  $\Delta C/C_0$  curves for CB1 and CB3 are notably flatter, with CB3 meeting the X9D specification, indicating significantly enhanced stability against temperature variations.

## Conclusion

- Doping with CuBi<sub>2</sub>O<sub>4</sub> lowers the sintering temperature to 1300 °C and promotes microstructural densification, demonstrating an excellent sintering-aid effect.
- CuBi<sub>2</sub>O<sub>4</sub> doping suppresses space-charge polarization, reduces dielectric loss, and yields a stable dielectric frequency response. All samples satisfy the X9P specification, while CB3 further meets the X9D standard, demonstrating improved stability against both frequency and temperature variations.
- CuBi<sub>2</sub>O<sub>4</sub> doping greatly enhances varistor characteristics, with higher nonlinear coefficient ( $\alpha$ ) and breakdown voltage ( $V_b$ ), and reduced leakage current ( $I_L$ ). This improvement stems from the n-p-n heterojunction formed at the LST/CuBi<sub>2</sub>O<sub>4</sub> interface, which raises the Schottky barrier height at the grain boundaries.